



POWER OF ATTORNEY

Inrange Technologies Corporation, located and doing business at 100 Mount Holly By-Pass, Lumberton, NJ 08048, has appointed and does hereby grant as its Attorney and Agent, Baker & Hostetler LLP, Washington Square, Suite 1100, 1050 Connecticut Avenue, and the following attorney(s) and/or agent(s) with full power of substitution and revocation, to prosecute the applications shown on the attached Schedule A, and to transact all business in the Patent and Trademark Office connected therewith:

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PATENT TRADEMARK OFFICE

AND In testimony whereof, the Assignor has signed below, by its duly authorized legal representative, effective the day executed below.

INRANGE TECHNOLOGIES CORPORATION

By: Kevin Lilly

Typed Name: Kevin Lilly

Title: Group General Counsel

Date: April 1, 2003

STATE OF New Jersey)
COUNTY OF Burlington) SS:

On this 1st day of April, 2003, before me personally appeared Kevin Lilly to me known to be the persons named in and who executed the above instrument, and acknowledged to me that they executed the same for the uses and purposes therein set forth.

S E A L

Shawn E. Coons
Notary Public

My commission expires July 29, 2007

SCHEDULE A

Patent Number	Application Number	Title	Issue Date	Attorney Date Filed	Attorney Date Issued

4,829,244	06/752,084	BUS AND TAG CABLE MONITORING TAP	05-Jul-1985	87264.1520	
5,394,503	09/196,959	OPTICAL FIBER CONNECTION MONITORING APPARATUS, PATCH PANEL CONTROL SYSTEM AND METHOD OF USING SAME	15-Feb-1994	87264.1640	
5,077,656	559,516	CPU CHANNEL TO CONTROL UNIT EXTENDER	23-Jul-1990	87264.1660	
4,764,939	088,476	CABLE SYSTEM FOR DIGITAL INFORMATION	19-Aug-1987	87264.1680	
6,188,702	09/428,753	HIGH SPEED LINKING MODULE	28-Oct-1999	87264.1700	
	09/739,081	HIGH SPEED LINKING MODULE	15-Dec-2000	87264.1701	
	09/856,079	HIGH SPEED LINKING MODULE	16-May-2001	87264.1706	
5,732,011	801,908	DIGITAL SYSTEM HAVING HIGH SPEED BUFFERING	14-Feb-1997	87264.1720	
5,434,845	197,464	INFREQUENT EVENT TRACE	16-Feb-1994	87264.1740	
4,661,966	777,021	METHOD AND APPARATUS FOR ADJUSTING TRANSMISSION RATES IN DATA CHANNELS FOR USE IN SWITCHING SYSTEMS	17-Sep-1985	87264.1760	
4,725,835	775,634	TIME MULTIPLEXED BUS MATRIX SWITCHING SYSTEM	13-Sep-1985	87264.1780	
4,804,956	846,910	REARRANGEABLE DIGITAL SIGNAL SPACE DIVISION SWITCHING SYSTEM	31-Mar-1986	87264.1800	
4,878,215	028,604	SIGNAL SWITCHING SYSTEM	20-Mar-1987	87264.1820	
5,784,386	674,839	FAULT TOLERANT SYNCHRONOUS CLOCK DISTRIBUTION	03-Jul-1996	87264.1840	
5,796,733	675,165	TIME DIVISION SWITCHING SYSTEM	03-Jul-1996	87264.1860	

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Patent Number	Application Number	Title	Filing Date	Issue Date
5,805,614	675,503	FAULT TOLERANT SWITCH FABRIC WITH CONTROL AND DATA CORRECTION BY HAMMING CODES	03-Jul-1996	87264.1880
5,812,556	675,491	FAULT TOLERANT SWITCH FABRIC WITH CONTROL AND DATA CORRECTION BY HAMMING CODES AND ERROR INDUCING CHECK REGISTER	03-Jul-1996	87264.1900
5,867,648	729,231	HIGH SPEED HETEROGENEOUS COUPLING OF COMPUTER SYSTEMS USING CHANNEL-TO-CHANNEL PROTOCOL	09-Oct-1996	87264.1920
4,683,564	443,193	MATRIX SWITCHING SYSTEM	22-Nov-1982	87264.1940
5,838,179	798,939	CLOCK COMPENSATION CIRCUIT	11-Feb-1997	87264.1960
6,023,180	09/195,419	CLOCK COMPENSATION CIRCUIT	17-Nov-1998	87264.1961
4,734,696	803,812	SYSTEM AND METHOD FOR TRANSMITTING INFORMATION	02-Dec-1985	87264.2000
	60/223,522	FIBRE CHANNEL ARCHITECTURE	07-Aug-2000	87264.2060
	09/922,591	FIBER CHANNEL ARCHITECTURE	03-Aug-2001	87264.2061
	US01/24524	FIBRE CHANNEL ARCHITECTURE	03-Aug-2001	87264.2062
	US01/22587	METHOD AND APPARATUS FOR IMPARTING FAULT TOLERANCE IN A SWITCH OR THE LIKE	03-Aug-2001	87264.2063
	09/920,751	METHOD AND APPARATUS FOR IMPARTING FAULT TOLERANCE IN A SWITCH OR THE LIKE	03-Aug-2001	87264.2064
	60/257,023	FIBRE CHANNEL PORT ADAPTER	20-Dec-2000	87264.2080
	10/022,195	FIBRE CHANNEL PORT ADAPTER	20-Dec-2001	87264.2081
	US01/48768	FIBRE CHANNEL PORT ADAPTER	20-Dec-2001	87264.2082

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Patent Number	Application Number	Title	Filing Date	Issue Date

	60/209,054	ENHANCED EET-3 CHANNEL ADAPTER CARD	02-Jun-2000	87264.2100
	09/872,778	ENHANCED CHANNEL ADAPTER	01-Jun-2001	87264.2101
	US01/17903	ENHANCED CHANNEL ADAPTER	01-Jun-2001	87264.2102
	60/258,437	DYNAMIC RESOURCE MAPPING IN A TCP/IP ENVIRONMENT	27-Dec-2000	87264.2120
	10/024,564	DYNAMIC RESOURCE MAPPING IN A TCP/IP ENVIRONMENT	21-Dec-2001	87264.2121
D453,146	29/122,449	FIBER OPTIC SWITCH	28-Apr-2000	87264.2220
6,236,193	09/587,952	APPARATUS FOR VOLTAGE REGULATION AND RECOVERY OF SIGNAL TERMINATION ENERGY	06-Jun-2000	87264.2340
	60/209,227	FIBRE CHANNEL ADDRESS ADAPTOR	02-Jun-2000	87264.2380
	60/209,226	FIBRE CHANNEL ADDRESS ADAPTOR	02-Jun-2000	87264.2381
	09/872,863	FIBRE CHANNEL ADDRESS ADAPTOR HAVING DATA BUFFER EXTENSION AND ADDRESS MAPPING IN A FIBRE CHANNEL SWITCH	01-Jun-2001	87264.2382
	US01/17951	FIBRE CHANNEL ADDRESS ADAPTOR HAVING DATA BUFFER EXTENSION AND ADDRESS MAPPING IN A FIBRE CHANNEL SWITCH	01-Jun-2001	87264.2383
	60/209,173	MESSAGE DIRECTOR	02-Jun-2000	87264.2400
	09/872,756	MESSAGE QUEUE SERVER SYSTEM	01-Jun-2001	87264.2401
	US01/17858	MESSAGE QUEUE SERVER SYSTEM	01-Jun-2001	87264.2402
	60/254,839	REAL TIME CALL TRACE CAPABLE OF USE WITH MULTIPLE ELEMENTS	12-Dec-2000	87264.2440

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Patent Number	Application Number	Title	Filing Date	Issue Date	Expiry Date
6,400,813	09/426,272	REAL TIME CALL TRACE CAPABLE OF USE WITH MULTIPLE ELEMENTS	12-Dec-2001	87264.2441	

6,400,813	09/426,272	REAL TIME CALL TRACE CAPABLE OF USE WITH MULTIPLE ELEMENTS	12-Dec-2001	87264.2441	
	US00/41490	MEDIATION SYSTEM FOR A TELEPHONE NETWORK	25-Oct-1999	87264.2460	
	60/254,091	COOLING SYSTEM FOR A TELECOMMUNICATIONS DEVICE	24-Oct-2000	87264.2461	
	09/817,542	COOLING SYSTEM	08-Dec-2000	87264.2500	
	60/230,839	PROTOCOL ANALYSIS FRAMEWORK	26-Mar-2001	87264.2501	
	09/919,353	PROTOCOL ANALYSIS FRAMEWORK	07-Sep-2000	87264.2520	
	60/126,142	CROSS MIDPLANE HIGH SPEED MATRIX SWITCH	31-Jul-2001	87264.2521	
	09/483,018	CROSS-MIDPLANE SWITCH TOPOLOGY	25-Mar-1999	87264.2540	
	US00/07490	CROSS-MIDPLANE SWITCH TOPOLOGY	13-Jan-2000	87264.2541	
	10/020,968	DEFERRED QUEUEING IN A BUFFERED SWITCH	22-Mar-2000	87264.2542	
	US02/40519	DEFERRED QUEUEING IN A BUFFERED SWITCH	19-Dec-2001	87264.2560	
	10/017,380	APPARATUS AND METHOD FOR PROVIDING DOMAIN NAME SERVICES TO MAINFRAME RESOURCE MAPPING 709	19-Dec-2002	87264.2561	
	10/026,707	METHOD AND APPARATUS FOR BOOTING A MICROPROCESSOR	18-Dec-2001	87264.2581	
	US02/41494	METHOD AND APPARATUS FOR BOOTING A MICROPROCESSOR	27-Dec-2001	87264.2600	
			27-Dec-2002	87264.2601	

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Patent Number	Application Number	Title	Issue Date	Filing Date
	10/004,878	METHOD AND APPARATUS FOR PROVIDING A VIRTUAL SHARED DEVICE	07-Dec-2001	87264.2620
	US02/38974	METHOD AND APPARATUS FOR PROVIDING A VIRTUAL SHARED DEVICE	06-Dec-2002	87264.2621
09/927,359		MULTIPLE ARBITRATION CIRCUIT	13-Aug-2001	87264.3000
US02/25516		MULTIPLE ARBITRATION CIRCUIT	13-Aug-2002	87264.3001
60/297,454		METHOD AND APPARATUS FOR RENDERING A CELL-BASED SWITCH USEFUL FOR FRAME BASED APPLICATION PROTOCOLS	13-Jun-2001	87264.3020
09/995,605		METHOD AND APPARATUS FOR RENDERING A CELL-BASED SWITCH USEFUL FOR FRAME BASED APPLICATION PROTOCOLS	29-Nov-2001	87264.3021
US02/18659		METHOD AND APPARATUS FOR RENDERING A CELL-BASED SWITCH USEFUL FOR FRAME BASED APPLICATION PROTOCOLS	13-Jun-2002	87264.3022
60/297,438		ADDRESS MAPPING AND IDENTIFICATION	13-Jun-2001	87264.3040
10/026,838		ADDRESS MAPPING AND IDENTIFICATION	27-Dec-2001	87264.3041
US02/18660		FOREIGN FILING OF ADDRESS MAPPING AND IDENTIFICATION	13-Jun-2002	87264.3042
60/297,439		PORT MIRRORING IN CHANNEL DIRECTORS AND SWITCHES	13-Jun-2001	87264.3060
10/026,706		PORT MIRRORING IN CHANNEL DIRECTORS AND SWITCHES	27-Dec-2001	87264.3061
US02/18655		PORT MIRRORING IN CHANNEL DIRECTORS AND SWITCHES	13-Jun-2002	87264.3062
6,141,701	09/038,603	SYSTEM FOR AND METHOD OF OFF-LOADING NETWORK TRANSACTIONS FROM A MAINFRAME TO AN INTELLIGENT INPUT/OUTPUT DEVICE, INCLUDING OFF-LOADING MESSAGE QUEUING FACILITIES	11-Mar-1998	87264.3080

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Patent Number	Publication Number	Title	Filing Date	Issue Date
	10/228,044	METHOD AND APPARATUS FOR DOWNLOADING EXECUTABLE CODE IN A NON-DISRUPTIVE MANNER	27-Aug-2002	87264.3100
	10/252,430	NON-DISRUPTIVE CODE LOAD IN REALTIME SYSTEM CONTAINING HARDWARE AND FIRMWARE	24-Sep-2002	87264.3120
	10/324,015	METHOD FOR DISTRIBUTING AND ACTIVATING SECURITY PARAMETERS IN NETWORK SWITCHES	20-Dec-2002	87264.3140
	60/424,333	METHOD AND APPARATUS FOR CONFIGURING A PROGRAMMABLE LOGIC DEVICE	07-Nov-2002	87264.3160
	10/299,007	METHOD AND APPARATUS FOR CONFIGURING A PROGRAMMABLE LOGIC DEVICES	19-Nov-2002	87264.3161
	10/305,365	DATA SIGNAL CABLE EMI/RF GROUNDING CLAMPING METHOD	27-Nov-2002	87264.3180
	60/424,334	METHOD AND APPARATUS FOR RESTRAINING A DATA CABLE	07-Nov-2002	87264.3200
	10/298,646	METHOD AND APPARATUS FOR RESTRAINING A DATA CABLE	19-Nov-2002	87264.3201